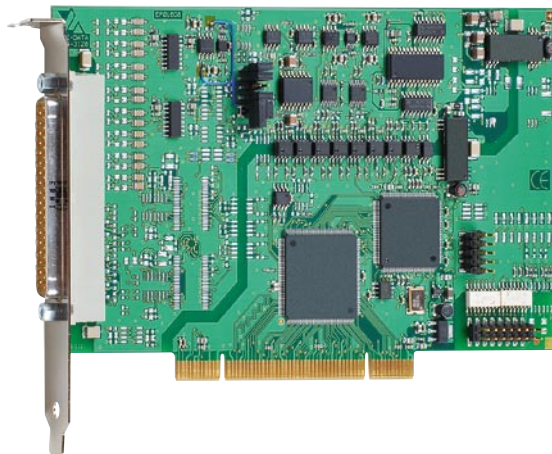


Analog input board, optically isolated, 16/8/4 SE or 8/4 differential inputs, 12-bit

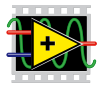


Also for
PCI EXPRESS
see APCLe-3121
page 96

Compatible version
for **CompactPCI™**
See CPCI-3001
page 194



Signed 64-bit drivers for
Windows 7/Vista/XP



LabVIEW™



LabWindows/CVI™



DASYLab™

Features

Analog inputs

- 16 single-ended / 8 differential inputs or 8 single-ended / 4 differential inputs or 4 single-ended inputs
- 12-bit resolution
- Throughput: 100 kHz
- Input voltage: 0-10 V, ± 10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V, 0-20 mA (option), freely programmable through software for each channel
- Gain PGA x1, x2, x5, x10 freely programmable through software for each channel
- PCI-DMA for analog data acquisition

Analog acquisition

- Single channel, several channels, several channels through scan list
- Automatic analog acquisition through cyclic timer control
- Acquisition through scan list: up to 16 entries with gain, channel, unipolar/bipolar
- Acquisition triggered through software, timer, external event
- Trigger functions:
 - Software trigger or
 - External trigger: the analog acquisition (single or scan) is started through signal switching from 0 V to 24 V at the digital input 0.
- Interrupt: End of single channel, end of multichannel, end of scan list

Digital

- 4 digital inputs, 24 V, optically isolated
- 4 digital outputs, 24 V, optically isolated

Timer

- 24-bit, can be used as cyclic time counter

Safety features

- Optical isolation 500 V min.
- Creeping distance IEC 61010-1
- Overvoltage protection ± 40 V
- Protection against high-frequency EMI
- Input filters: 159 kHz
- Noise neutralisation of the PC supply

APCI-3001

16/8/4 single-ended or
8/4 differential inputs

12-bit resolution

Optical isolation 500 V

100 kHz throughput

PCI DMA, programmable gain

8 digital I/O, 24 V, optically isolated, timer

Trigger functions

Applications

- Industrial process control
- Industrial measurement and monitoring
- Multichannel data acquisition
- Control of chemical processes
- Factory automation
- Acquisition of sensors
- Laboratory equipment
- Current measurement
- Instrumentation

Software drivers

A CD-ROM with the following software and programming examples is supplied with the board.

Standard drivers for:

- Linux (real-time)
- 32-bit drivers for Windows 7/Vista/XP/2000 (real-time)
- Signed 64-bit drivers f. Windows 7/Vista/XP

Drivers and samples for the following compilers and software packages:

- Visual C++ • Microsoft C
- Borland C++ • Borland C
- Visual Basic • Delphi
- LabVIEW • DASYLab • DIAdem

On request:

Further operating systems, compilers and samples.

Driver download: www.addi-data.com, download menu

Specifications

Analog inputs

Number of inputs:	16 single-ended/8 differential inputs 8 single-ended/4 differential inputs or 4 single-ended inputs
Resolution:	12-bit
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input ranges:	Software-programmable for each channel 0-10 V, ±10 V, 0-5 V, ± 5 V, 0-2 V, ± 2 V, 0-1 V, ± 1 V 0-20 mA optional
Throughput:	100 kHz
Gain:	Software programmable (x1, x2, x5, x10)
Common mode rejection:	DC at 10 Hz, 90 dB minimum
Relative precision (INL):	± 1 LSB (ADC)
Diff. non-linearity (DNL):	± 0.5 LSB (ADC)
Input impedance (PGA):	10 ¹² Ω // 10 nF single-ended, 10 ¹² Ω // 20 nF differential against GND
Bandwidth (- 3 dB):	Limited to 159 kHz with low-pass filter
Trigger:	Through software, timer, external event (24 V input)
Data transfer:	Data to the PC through FIFO memory, I/O commands, interrupt at EOC (End Of Conversion) and EOS (End of Scan), DMA transfer at EOC
Interrupts:	End of conversion, at timer overrun, End of scan

Timer

Time base timer 2: 50 µs; smallest programmable value: 100 µs

Digital I/O

Number of I/O channels:	4 digital inputs, 4 digital outputs, 24 V
Optical isolation:	500 V through opto-couplers from PC to peripheral
Input range:	0-30 V - Logical "0": 0-5 V - Logical "1": 10-30 V
Input current at 24 V:	3 mA typ.
Output range:	5-30 V
Max. switching current:	10 mA typ.
Output type:	Open Collector

EMC – Electromagnetic compatibility

The product complies with the European EMC directive. The tests were carried out by a certified EMC laboratory in accordance with the norm from the EN 61326 series (IEC 61326). The limit values as set out by the European EMC directive for an industrial environment are complied with. The respective EMC test report is available on request.

Physical and environmental conditions

Dimensions:	175 x 99 mm
System bus:	PCI 32-bit 3.3 / 5 V acc. to specification 2.1 (PCISIG)
Space required:	1 PCI slot for analog inputs, 1 slot opening for digital I/O
Operating voltage:	+5 V, ±5 % from the PC
Current consumption:	496 mA typ. ± 10 %
Front connector:	37-pin D-Sub male connector
Additional connector:	16-pin male connector for ribbon cable for connecting the digital inputs and outputs
Temperature range:	0 to 60 °C (with forced cooling)

APCI-3001

Analog input board, optically isolated, 16/8/4 SE or 8/4 diff. inputs, 12-bit. Incl. technical description and software drivers.

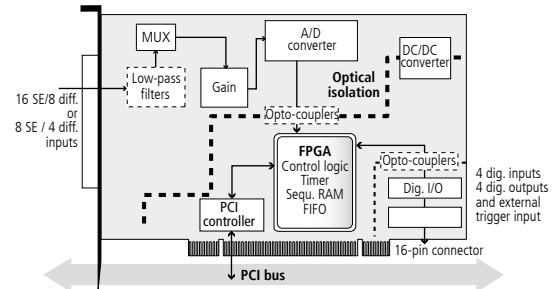
Versions

- APCI-3001-16:** 16 SE/8 diff. inputs, 8 dig. I/O
- APCI-3001-8:** 8 SE/4 diff. inputs, 8 dig. I/O
- APCI-3001-4:** 4 SE inputs, 8 dig. I/O

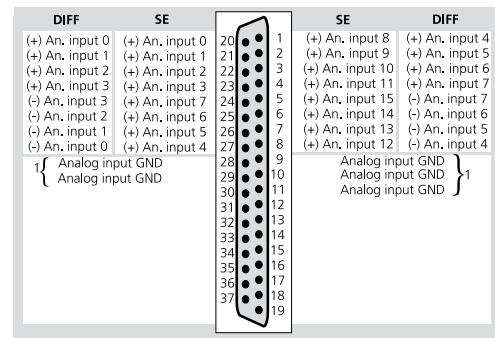
Options

- Please indicate the number of channels
- Option SF:** Precision filter for 1 single-ended channel
- Option DF:** Precision filter for 1 differential channel
- Option SC:** Current input for 1 single-ended channel 0(4)-20 mA
- Option DC:** Current input for 1 diff. channel, 0(4)-20 mA

Simplified block diagram

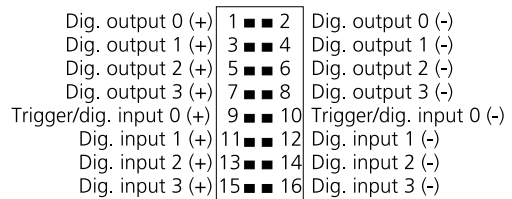


Pin assignment – 37-pin D-Sub male connector

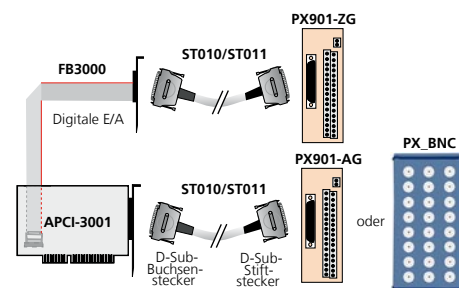


1: The analog inputs have a common ground line

Pin assignment – 16-pin male connector



ADDI-DATA connection

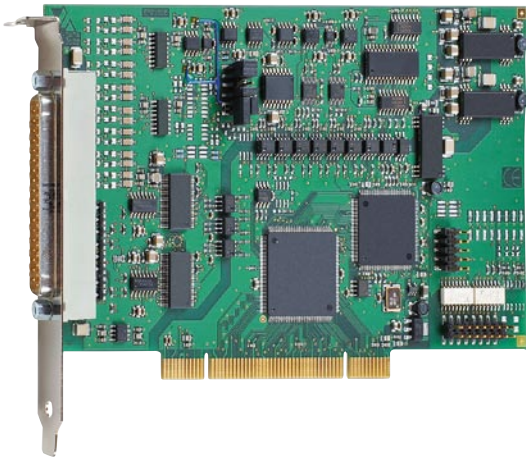


Ordering information

Accessories

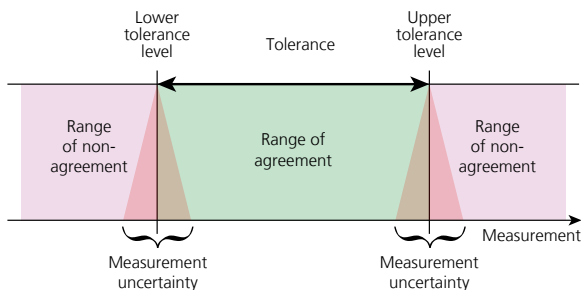
- PX901-A:** Screw terminal panel with transorb diodes, for connecting the analog inputs
- PX901-AG:** Same as PX901-A with housing for DIN rail
- PX_BNC:** BNC connection box for connecting the analog I/O
- PX901-ZG:** Screw terminal panel for connecting the digital I/O, for DIN rail
- ST010:** Standard round cable, shielded, twisted pairs, 2 m
- ST011:** Standard round cable, shielded, twisted pairs, 5 m
- FB3000:** Ribbon cable for digital I/O

PCI BOARDS: ANALOG I/O



High-precision measurement in the field

There are numerous measurement systems that provide precise data under laboratory conditions. This is different in the production or in outdoor areas, where the conditions are considerably more demanding. There the measurement results play a central role. Rework and defective goods can only be effectively reduced if the tolerance testing during the production process is precise enough to sort products reliably as being within or without the tolerance range. The importance of accuracy appears even clearer in cases where the measurement is used for regulation.



Precise even in case of temperature drift

Temperature drift can be caused by the surrounding temperature as well as by the board itself. To ensure the accuracy of the measurement, on the one hand we are careful to use only high-quality components with little drift. On the other hand we pay a lot of attention to the board layout. For example, components that generate heat are placed where they will not heat up the other components unnecessarily.

Precision through interference resistance

Not only the quality of the A/D converter is important but the interference resistance of the whole chain of acquisition has to be analysed, from the sensors to the acquisition board. Therefore in addition to our PC boards we offer robust cables and screw terminal boards that are intended for the use in a harsh industrial environment.

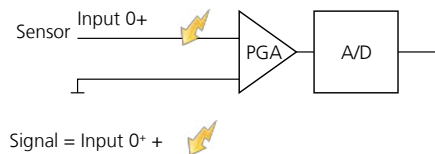
High-precision measurement

Measurement technology is the basis of every automation process. Therefore the accuracy of the sensor acquisition is highly important. The environment of a production line with a lot of interference requires peak performance from the measurement technology. For more than 25 years, ADDI-DATA has been developing analog boards for data acquisition intended for an industrial environment: they are robust, precise and fast.

Single-ended or differential inputs

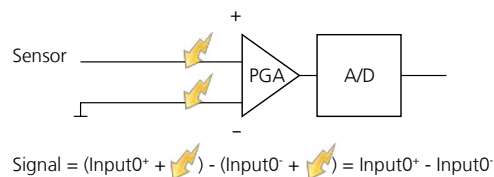
When measuring input voltage you can choose between 2 modes with important differences:

Single-ended mode – One-wire inputs connected to the system ground.



When acquiring analog signals in the single-ended mode, interfering signals are acquired with the signal. Therefore this mode is only advisable in case of high voltage levels and short lines.

Differential mode – Two-wire inputs



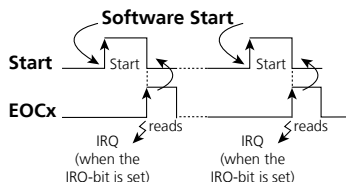
Interference signals affecting both lines are not included in the measurement because of the difference on the input. This is the optimal mode for measurement lines with a lot of interferences and long lines.

Acquisition modes of the analog inputs

There are four modes available for analog inputs. The following is a short overview of the settings available for data acquisition:

A. Simple mode

The software initiates and starts the A/D conversion and reads out the digital values of one or more channels after the end of conversion.



B. Sequence modes

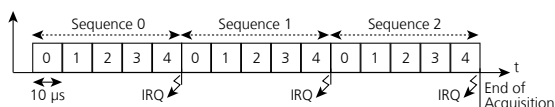
With the DMA function (Direct Memory Access) for a direct data exchange with the PC memory

There are 2 available sequence modes:

1. Simple sequence mode

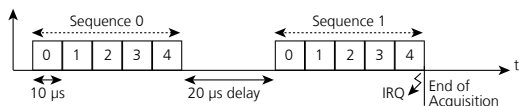
In this example the interrupt is generated at the end of each sequence after 5 acquisitions.

The complete acquisition process ends after 3 sequences.



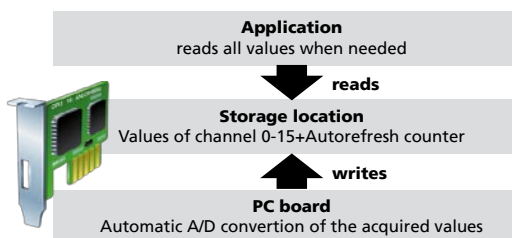
2. Sequence mode with delay

In this example the delay time between the end of one sequence and the start of the next sequence is 20 μs.



C. Auto refresh mode

The analog acquisition is initialised and the values of the channels are written in a buffer on an analog board. The PC reads the data asynchronously to the acquisition.

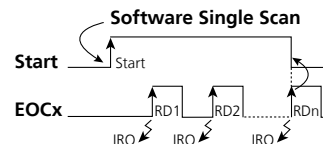


D. Scan mode

There are 6 different scan modes:

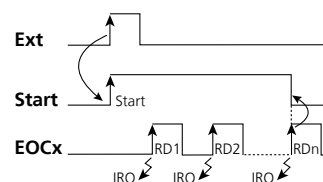
1. Software single scan

The interrupt routine of the user is called up after the last IRQ.

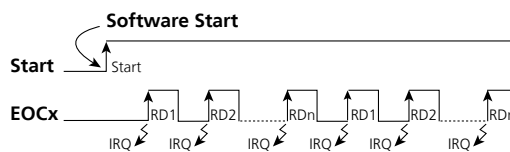


2. Hardware triggered single scan

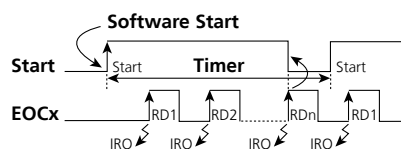
This scan can be triggered with increasing or decreasing edge. The hardware trigger allows to start the acquisition independently from the software or to start the acquisition of more than one board at the same time.



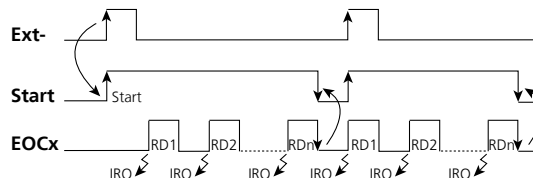
3. Continuous scan (software)



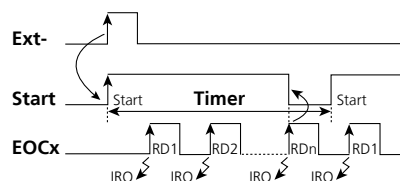
4. Continuous scan with timer delay (software)



5. Continuous scan (hardware)

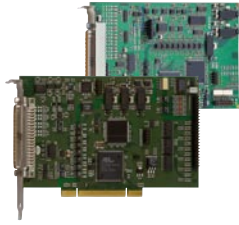


6. Continuous scan with timer delay (hardware)



PCI BOARDS: ANALOG I/O

Multifunction, analog input and analog output boards for 3.3 V or 5 V PCI








	Multifunction boards			Analog input boards					Analog output boards		
	APCI-3120	APCI-3110	APCI-3116	APCI-3010	APCI-3016	APCI-3002	APCI-3003	APCI-3001	APCI-3504	APCI-3504C	APCI-3501
32-bit PCI bus	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	3.3/5 V	5 V	5 V	3.3/5 V
FPGA	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
Simultaneous acquisition							✓				
Analog inputs											
Single Ended	16/8	16/8	16/8	16/8/4	16/8/4			16/8/4			
Differential	8/4	8/4	8/4	8/4/2	8/4/2	16		8/4			
Diff. separated from each other							4				
Resolution (-bit)	16	12	16	12	16	16	16	12			
Optical isolation	500 V	✓	✓	✓	✓	✓	✓	✓			
Throughput (kHz)	100	200	200	200	200	200	400/ channel	100			
Voltage range											
0-10 V; ± 10 V / 0-5 V; ± 5 V 0-2 V; ± 2 V / 0-1 V; ± 1 V	✓	✓	✓	✓	✓	✓	✓	✓			
Other ranges (optional)	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA	0-20 mA			
Gain 1, 2, 5, 10	✓	✓	✓	✓	✓	✓	✓	✓			
FIFO (value)	256	512	512	512	512	512	1024	256			
Functions of the analog inputs											
DMA (scatter gather, single, continuous, Sequence)		✓	✓	✓	✓	✓	✓	✓			
DMA (single, continuous, Sequence)	✓							✓			
Auto Refresh		✓	✓	✓	✓	✓	✓	✓			
Interrupt	✓	✓	✓	✓	✓	✓	✓	✓			
Programmed I/O	✓	✓	✓	✓	✓	✓	✓	✓			
Trigger:											
Software	✓	✓	✓	✓	✓	✓	✓	✓			
TTL input	-	-	-	-	-	-	-	-			
24 V input	✓	✓	✓	✓	✓	✓	✓	✓			
Sequence RAM	✓	✓	✓	✓	✓	✓	✓	✓			
Analog outputs	4 or 8	4	4						4	4	4 or 8
Resolution (-bit)	14	12	12						12	12	14
Optical isolation	✓	✓	✓						✓	✓	✓
0-10 V ± 10 V	✓	✓	✓						✓	✓	✓
Current outputs										0-20 mA	
Setup time	30 µs	15 µs	15 µs						15 µs	70 µs	30 µs
Digital I/O											
24 V inputs, optically isolated	4	4	4	4	4	4	4	4			
24 V outputs, optically isolated	4(OpenC)	4(50mA)	4(50mA)	4(50mA)	4(50mA)	4(50mA)	4(50mA)	4(OpenC)			2 2(OpenC)
TTL I/O		24	24	24	24						
Timer/Counter/Watchdog (depth) and/or	1 / - / 1 24-bit	3 / 3 / 2 16-bit	3 / 3 / 2 16-bit	3 / 3 / 1 16-bit	3 / 3 / 1 16-bit	1 / - / - 16-bit	1 / - / - 16-bit	1 / - / - 16-bit	1 / - / - 12-bit	1 / - / - 12-bit	1 / - / - 12-bit
Page	138	140	140	142	142	144	146	148	150	150	152
Software	Current driver list on the web: www.addi-data.com										



Temperature, pressure, noise, vibration and length measurement

New!

	Temperature measurement	Pressure measurement	Noise and vibration measurement	Length measurement	
					
	APCI-3200	APCI-3300	APCI-3600	APCI-3702	APCI-3701
64-bit or 32-bit PCI-Bus	3.3 V / 5 V	3.3 V / 5 V	3,3 V / 5 V	3.3 V / 5 V	3.3 V / 5 V
FPGA			✓		
Noise and vibration			✓		
Thermocouples J,K,T,E,R,S,B,N Pt100, Pt1000	✓				
Strain gauges		✓			
Inductive transducers				Half Bridge, LVDT	Half Bridge, LVDT
Signal conditioning			8 current sources for connecting ICP™ sensors		
Analog inputs	4 groups 4 channels	4 groups 4 channels			
Single Ended (SE)/ differential (diff.)	16 thermo/8 RTDs 8 thermo/4 RTDs 4 thermo/2 RTDs	8/4 inputs for strain gauges	8/8	5 channels <i>simultaneous</i> acquisition for induct. displacement transducers	16/8/1 channels for inductive displacement transducers
Resolution (-bit)	18	18	24	16	16
Optical isolation	✓	✓			
Throughput	20-160 Hz	20-160 Hz	2-200 kHz (through software)	depends on transducer type	depends on transducer type 2-20 kHz (50 kHz opt.)
Voltage ranges	+ 1.25 V	+ 1.25 V	± 10 V		
Gain	1, 2, 4, 8, 16, 32, 64, 128	1, 2, 4, 8, 16, 32, 64, 128		depends on transducer type	depends on transducer type
FIFO (Values)			128 DWORD		
Functions of the analog inputs					
DMA (scatter gather; single; continuous; Sequence)			scatter gather free run, ring buffer	✓	✓
Auto Refresh				✓	✓
Interrupt	✓	✓	✓	✓	✓
Programmed I/O	✓	✓	✓	✓	✓
Trigger: Software 24 V input	✓ ✓	✓ ✓	✓ ✓	✓ ✓	✓ ✓
Sequence RAM				✓	✓
Analog outputs					
Resolution (-bit)			2		
Optical isolation			16		
0-10 V ± 10 V			± 10 V		
Chronometer inputs Gate inputs			4 2		
Timer/Watchdog (depth) in combination, and/or	- / -	- / -	- / -	1 / - 16-bit	1 / - 16-bit
Digital I/O Inputs 24 V, optically isolated Outputs 24 V, optically isolated	4 3 (Open Collector)	4 3 (Open Collector)	8 8 (50 mA)	8 8 (125 mA)	8 8 (125 mA)
Page	154	158	160	164	166
Software	Current driver list on the web: www.addi-data.com				